

ICAST 2026 Conference invitation - Co-Organised by IIT Madras and SCSVMV



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Date 2026-09-14 14:52

ICAST 2026 Conference.pdf (~1005 KB)

Dear Faculty Members, Research Scholars and Students,

Greetings. I am sure you all will be very interested to know about a unique interdisciplinary Conference on Semiconductors in India this year.

What is it?

It is the **International Conference on Advanced Semiconductor Technologies (ICAST 2026)**, organised by the **Indian Institute of Technology Madras (IIT Madras)** in association with the **Sri Chandrasekharendra Saraswathi Viswa Mahavidyalaya (SCSVMV) Enathur Kancheepuram**. Brochure **attached** for your reference.

Where and When?

IIT MADRAS, ICSR Building, Chennai, India, 13th – 15th December 2026

Why is it unique?

ICAST 2026 explores and connects - perspectives in Semiconductor devices and packaging technologies with emerging applications, AI and computational technologies, Indian Knowledge Systems, emerging technology education and sustainable semiconductor ecosystem development, aligned with the **Indian Semiconductor Mission ISM 2.0 goals**.

The Conference seeks to create a vibrant forum for innovation, collaboration and capacity building, contributing towards the development of a strong and sustainable Indian Microelectronics industry, through expert sessions, technical deliberations, industry interaction and specialised learning opportunities.

There are Pre-conference workshops on 13th December on: **Power Device Technology; Packaging Technology; Advanced Materials; Testing & Reliability; Hands-on labs and demonstrations**, followed by the major technical tracks scheduled for 14th - 15th December on: **Semiconductor Devices, Semiconductor Technology, Artificial Intelligence, Education, Sustainability, Sanskrit & Ancient Indian Knowledge Systems for the Future of Hardware and Software Technologies, and Semiconductor Packaging**.

What is our wish?

We are very pleased to invite you all to this unique opportunity to actively participate and present your **research papers and posters** at the **International Conference on Advanced Semiconductor Technologies (ICAST 2026)**, organised by the **Indian Institute of Technology Madras (IIT Madras)** in association with the **Sri Chandrasekharendra Saraswathi Viswa Mahavidyalaya (SCSVMV)**.

We request you to make use of this prestigious opportunity to **showcase your research, interact with eminent academicians and industry experts, establish research collaborations, and gain exposure** to the rapidly emerging Indian Semiconductor ecosystem towards **Viksit Bharat 2047 goals**.

We encourage all interested **Faculty members, Research scholars and Students** to submit their **original research papers and poster presentations** in areas relevant to the conference themes below:

14 December 2026

Track A – Semiconductor Devices:

- Wide Bandgap Semiconductor Materials and Epitaxy
- Power Semiconductor Device Design and Fabrication
- Emerging Applications of Power Semiconductor Devices
- Advanced Semiconductor Materials and Devices
- Advances in Power Semiconductor Devices

Track B – Special tracks:

- AI & Computational Linguistics for the Semiconductor Industry
- Sanskrit & Ancient Indian Knowledge Systems for the Future of Hardware and Software Technologies
- Sustainability Challenges in Semiconductor Fabrication Processes Semiconductor
- Industry Supply Chain Challenges: Critical Chemicals | Materials | Utilities | Compliance | Global Policy
- Education & Training for Building a Sustainable Semiconductor Workforce Towards the Viksit Bharat Semiconductor Ecosystem

15 December 2026

Track C – Semiconductor Packaging:

- Advanced Power Packaging Technology
- Advanced Packaging Methods
- Packaging Materials & Manufacturing
- Package Design, Modelling and Simulation
- Package Testing and Reliability

Industry and Emerging Technologies

8
24/9/26

- Smart Manufacturing in Semiconductor Packaging
- Semiconductor Manufacturing Technologies
- Advanced Materials and Reliability
- Design, Testing and Simulation
- Industry Applications and Technology Trends

Important Dates

- **Paper Submission Deadline:** 30 September 2026
- **Acceptance Notification:** 20 October 2026
- **Camera-Ready Paper:** 10 November 2026
- **Conference:** 14–15 December 2026

For further details regarding paper submission and the conference programme, participants may refer to the <https://kanchiuniv.ac.in/icast2026/> website. The conference contact email is icast2026@kanchiuniv.ac.in

Conference mode

Indian Participants: Physical Mode | Foreign Participants: Hybrid Mode

Venue

IIT MADRAS, ICSR Building, Chennai, India

Registration Fee Details:

Category	Indian Participants	Foreign Participants
Students / Research Scholars	Rs. 5000	USD 100
Faculty/Academicians	Rs. 7000	USD 200
Industry Delegates	Rs. 10000	USD 250

We look forward to your active engagement and participation in making ICAST 2026 a Grand Success.

Thanking you,

ICAST 2026 Organising Committee, IIT Madras and SCSVMV

ABOUT THE CONFERENCE

The Conference is envisioned as a premier platform bringing together academia, industry, researchers, technology professionals and students to explore emerging developments and opportunities in the rapidly evolving semiconductor ecosystem. With a focus on semiconductor devices, advanced materials, power electronics, packaging technologies, testing, reliability and smart manufacturing, the Conference aims to promote meaningful knowledge exchange and collaboration across the semiconductor value chain. The programme also addresses critical future-oriented themes including Artificial Intelligence, sustainability in semiconductor manufacturing, supply-chain challenges, curriculum alignment and workforce development. A distinctive feature of the Conference is its Special Track, which explores interdisciplinary perspectives connecting AI and computational technologies, Indian Knowledge Systems, emerging technology education and sustainable semiconductor development. Through expert sessions, technical deliberations, industry interaction and specialised learning opportunities, the Conference seeks to create a vibrant forum for innovation, collaboration and capacity building, contributing towards the development of a strong and sustainable semiconductor ecosystem in India.

ABOUT IIT MADRAS

Indian Institute of Technology Madras (IIT Madras) is a premier institution of higher learning and research, playing a significant role in advancing science, technology, innovation and national development. Through its strong academic and research ecosystem, IIT Madras continues to contribute to cutting-edge technological advancements, industry collaboration and the development of future-ready talent. As the Organiser of ICAST 2026, IIT Madras brings together leading academicians, researchers, industry experts and institutions to create a meaningful platform for knowledge exchange and technological collaboration in the rapidly evolving semiconductor domain.

ABOUT SCSVMV

Sri Chandrasekharendra Saraswathi Viswa Mahavidyalaya (SCSVMV), Enathur, Kanchipuram, is a distinguished institution committed to excellence in education, research, innovation and interdisciplinary learning. Guided by the vision of harmoniously integrating India's rich knowledge traditions with contemporary scientific and technological education, the University continues to foster an environment that encourages intellectual exploration and innovation.

In association with IIT Madras, SCSVMV is proud to contribute to ICAST 2026, supporting the Conference's vision of promoting academic excellence, research collaboration and capacity building in emerging semiconductor technologies. The involvement of the University's institutional leadership in the Advisory Committee further reflects SCSVMV's association with the Conference.

Advisory Committee

Chair (Advisory Committee)

Prof. V. Kamakoti, Director, Indian Institute of Technology Madras

Institutional Advisory Members (SCSVMV)

Prof. Vempathy Kutumba Sastry, Chancellor, SCSVMV University

Prof. G. Srinivas, Vice-Chancellor, SCSVMV University

Prof. R. Vasanth Kumar Mehta, Pro Vice-Chancellor

Prof. G. Sirram, Registrar

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Prof. V. A. L. Roy, University of Glasgow

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Dr. Sameer Sharma, Former Chief Secretary, Government of Andhra Pradesh

Shri N. Prabhakaran, Scientist 'G', GIRE, Bengaluru

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Mr. Sharat Kaul, Global Advanced Packaging & OSAT Consultant

Senior Technical Director, Intel India

Senior Scientist, Texas Instruments India

Principal Engineer, Qualcomm India

Technology Expert, Applied Materials

Senior Researcher, Micron Technology

INTERNATIONAL CONFERENCE ON ADVANCED SEMICONDUCTOR TECHNOLOGIES
ICAST 2026
POWER SEMICONDUCTORS AND APPLICATIONS

13TH - 15TH DECEMBER 2026
 IIT MADRAS CHENNAI

ADVANCED DEVICES
 FABRICATION & MATERIALS
 AI & DIGITAL TECH
 ADVANCED PACKAGING
 POWER SEMICONDUCTORS
 SUSTAINABLE MANUFACTURING

BRIDGING HERITAGE, INTELLIGENCE & SEMICONDUCTOR INNOVATION

IIT MADRAS
 CHENNAI - 600036

SCSVMV
 DEEMED UNIVERSITY
 KANCHIPURAM - 631561

ORGANIZED BY

INDIAN INSTITUTE OF TECHNOLOGY MADRAS

in association with

**SRI CHANDRASEKHARENDRA SARASWATHI
 VISWA MAHAVIDYALAYA**

CONFERENCE PARTNERS



Organizing Committee

IIT MADRAS

- Dr. Anbarasu M., Dept of EEE, IIT MADRAS
- Dr. Ganesan K., Dept of EEE, IIT MADRAS
- Dr. Ravindra, Dept of EEE, IIT MADRAS

SCSVMV

- Dr. M. Rathnakumar, Dept. of EEE, SCSVMV
- Dr. G. Senthil Kumar, Dept. of EEE, SCSVMV
- Dr. R. Malathi, Dept. of EEE, SCSVMV
- Dr. P. Venkatesan, Dept of EEE, SCSVMV
- Dr. K. Anisha, Dept of EEE, SCSVMV
- Mr. T. Prakash, Dept of EEE, SCSVMV
- Mr. Chaitanya Kumar M., Dept of Management Studies, SCSVMV

re-Conference Hands-on Workshops

13 December 2026

- Power Device Technology
- Packaging Technology
- Advanced Materials
- Testing & Reliability
- Hands-on Labs and Demonstrations

RACK A: SEMICONDUCTOR DEVICES

14 December 2026

- Wide Bandgap Semiconductor Materials and Epitaxy
- Power Semiconductor Device Design and Fabrication
- Emerging Applications of Power Semiconductor Devices
- Advanced Semiconductor Materials and Devices
- Advances in Power Semiconductor Devices

TRACK B – SPECIAL TRACKS / TOPICS

- AI & Computational Linguistics for the Semiconductor Industry
- Sanskrit & Ancient Indian Knowledge Systems for the Future of Hardware and Software Technologies
- Aligning Emerging Technology Requirements with Academic Curriculum in VLSI Design | Embedded Systems | AI/ML | Future Skills
- Sustainability Challenges in Semiconductor Fabrication Processes
- Supply Chain Challenges in Semiconductor Industry
- Critical Chemicals | Materials | Utilities | Compliance | Global Policy
- Education & Training for Building a Sustainable Semiconductor Workforce Towards the Viksit Bharat Semiconductor Ecosystem

TRACK C: SEMICONDUCTOR PACKAGING

On 15 December 2026

- Advanced Power Packaging Technology
- Advanced Packaging Methods
- Packaging Materials & Manufacturing
- Package Design, Modeling and Simulation
- Package Testing and Reliability
- Smart Manufacturing in Semiconductor Packaging

INDUSTRY & EMERGING TECHNOLOGIES

- Smart Manufacturing
- Semiconductor Manufacturing Technologies
- Advanced Materials and Reliability
- Design, Testing and Simulation
- Industry Applications and Technology Trends

Call for Papers

ICAST 2026 invites original research contributions, review articles and industry papers in all areas of semiconductor technologies. Submit your papers through the conference website. Accepted to be published in the conference proceedings.

Paper submission through

<https://kanchiuniv.ac.in/icast2026/#programme>

IMPORTANT DATES

Paper submission deadline : 30 September 2026
Acceptance Notification : 20 October 2026
Camera-Ready paper : 10 November 2026
Conference Dates : 13 -15 December 2026

Registration Fee Details

Category	Indian Participants	Foreign Participants
Students / Research Scholars	Rs.5000	USD 100
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CONFERENCE MODE

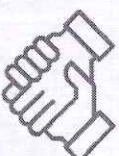
Indian Participants: Physical Mode | Foreign Participants: Hybrid Mode

Venue

IIT MADRAS, ICSR Building, Chennai, India

CONTACT DETAILS

✉ icast2026@kanchiuniv.ac.in



“Together, Let Us Shape the Future of Semiconductor Technology.”